**Course Title: Digital Logic and System Design**

**Course Code: CSE 210**

**Credit Hour: 1.5**

**Experiment No. 1**

**Experiment Name: Test and verify the truth table of following gates: AND, OR, NOT, NAND, NOR, XOR**

**Tasks:**

1. Test and verify the truth table of following gates:

(a) AND,

(b) OR,

(c) NOT,

(d) NAND,

(e) NOR,

(f) XOR